



STANLEY
COLLEGE OF ENGINEERING & TECHNOLOGY FOR WOMEN
(Approved by AICTE, New Delhi | Affiliated to Osmania University ,Hyderabad)
Address : Chapel Road, Abids ,Hyderabad

EE 383

INTEGRATED CIRCUITS LAB

(Common to IE & EEE)

LIST OF EXPERIMENTS:

1. Generation of triangular, sine and square wave using IC's.
2. PLL (Phase locked loop).
3. Design of a stable multivibrator using 555 timer.
4. Active filters.
5. Instrumentation amplifier --Sample and hold circuit.
6. Design of integrator and differentiator using Op-Amp.
7. Multiplexer --application for logic realization and parallel to serial Conversions.
8. Synchronous counters.
9. Asynchronous counters.
10. Clippers and clampers using Op-Amps.
11. Monostable operation using IC's.
12. Boot-strap sweep circuit using Op-Amp.
13. Half adder, full adder and subtractor and realization of combinational logic.
14. A/D converters.
15. D/ A converters.

Note: At Least Ten Experiments Should Be Conducted In The Semester.